

MITSUBISHI THYRISTOR MODULES
TM55DZ/CZ-24,-2H

HIGH VOLTAGE HIGH POWER GENERAL USE
 INSULATED TYPE

TM55DZ/CZ-24,-2H



- **IT (AV)** Average on-state current **55A**
- **VRRM** Repetitive peak reverse voltage
 **1200/1600V**
- **VDRM** Repetitive peak off-state voltage
 **1200/1600V**
- **DOUBLE ARMS**
- **Insulated Type**
- **UL Recognized**

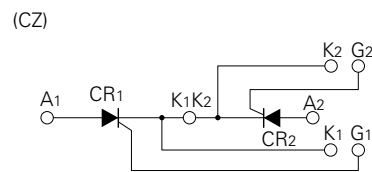
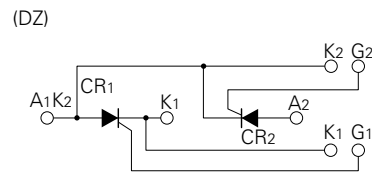
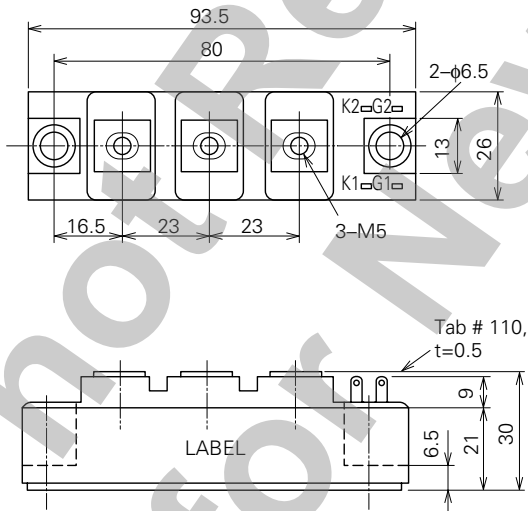
Yellow Card No. E80276 (N)
 File No. E80271

APPLICATION

DC motor control, NC equipment, AC motor control, Contactless switches,
 Electric furnace temperature control, Light dimmers

OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Voltage class		Unit
		24	2H	
VRRM	Repetitive peak reverse voltage	1200	1600	V
VRSM	Non-repetitive peak reverse voltage	1350	1700	V
VR (DC)	DC reverse voltage	960	1280	V
VDRM	Repetitive peak off-state voltage	1200	1600	V
VDSM	Non-repetitive peak off-state voltage	1350	1700	V
VD (DC)	DC off-state voltage	960	1280	V

Symbol	Parameter	Conditions	Ratings	Unit
IT (RMS)	RMS on-state current		86	A
IT (AV)	Average on-state current	Single-phase, half-wave 180° conduction, Tc=81°C	55	A
ITSM	Surge (non-repetitive) on-state current	One half cycle at 60Hz, peak value	1100	A
I ² t	I ² t for fusing	Value for one cycle of surge current	5.0 × 10 ³	A ² s
di/dt	Critical rate of rise of on-state current	V _D =1/2V _{DRM} , I _G =1.0A, T _j =125°C	100	A/μs
PGM	Peak gate power dissipation		5.0	W
PG (AV)	Average gate power dissipation		0.5	W
VFGM	Peak gate forward voltage		10	V
VRGM	Peak gate reverse voltage		5.0	V
IFGM	Peak gate forward current		2.0	A
T _j	Junction temperature		-40~+125	°C
T _{stg}	Storage temperature		-40~+125	°C
Viso	Isolation voltage	Charged part to case	2500	V
—	Mounting torque	Main terminal screw M5	1.47~1.96	N·m
			15~20	kg·cm
		Mounting screw M6	1.96~2.94	N·m
			20~30	kg·cm
—	Weight	Typical value	160	g

ELECTRICAL CHARACTERISTICS

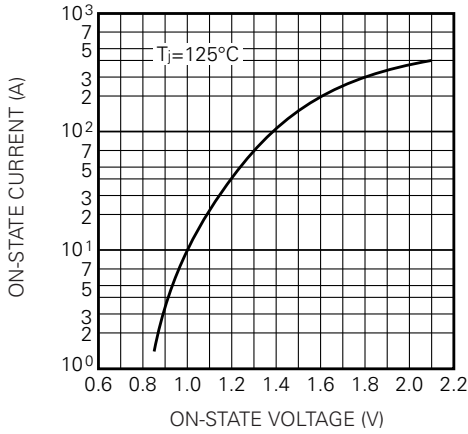
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
IRRM	Repetitive peak reverse current	T _j =125°C, VRRM applied	—	—	10	mA
IDRM	Repetitive peak off-state current	T _j =125°C, VDRM applied	—	—	10	mA
VTM	On-state voltage	T _j =125°C, I _{TM} =165A, instantaneous meas.	—	—	1.5	V
dv/dt	Critical rate of rise of off-state voltage	T _j =125°C, V _D =2/3V _{DRM}	500	—	—	V/μs
VGT	Gate trigger voltage	T _j =25°C, V _D =6V, R _L =2Ω	—	—	2.0	V
VGD	Gate non-trigger voltage	T _j =125°C, V _D =1/2V _{DRM}	0.25	—	—	V
I _{GT}	Gate trigger current	T _j =25°C, V _D =6V, R _L =2Ω	15	—	100	mA
R _{th (j-c)}	Thermal resistance	Junction to case (per 1/2 module)	—	—	0.5	°C/W
R _{th (c-f)}	Contact thermal resistance	Case to fin, conductive grease applied (per 1/2 module)	—	—	0.2	°C/W
—	Insulation resistance	Measured with a 500V megohmmeter between main terminal and case	10	—	—	MΩ

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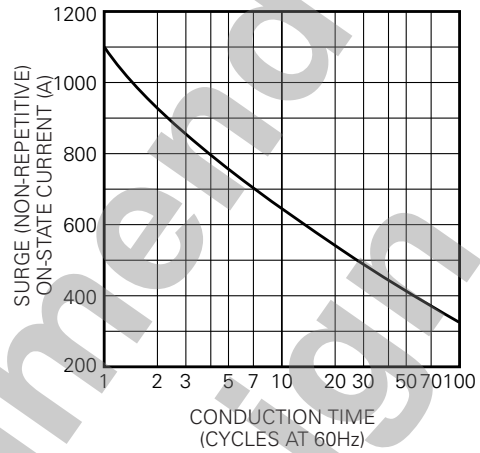
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PERFORMANCE CURVES

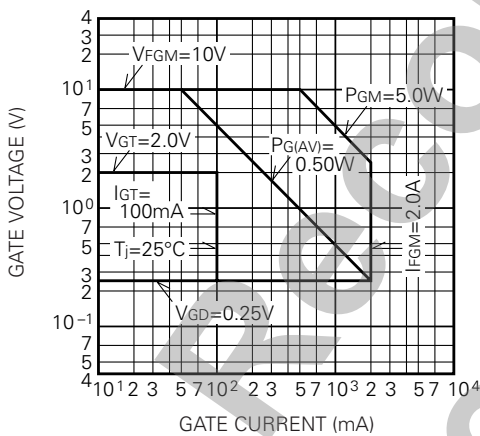
MAXIMUM ON-STATE CHARACTERISTIC



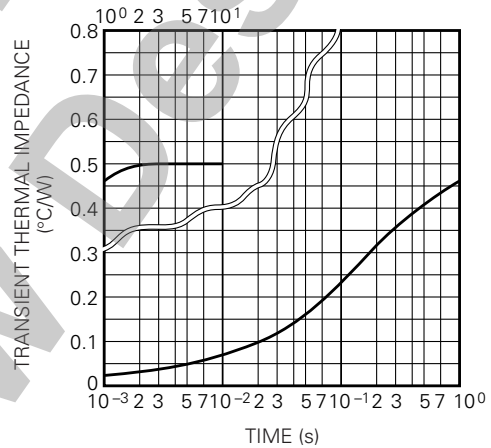
RATED SURGE (NON-REPETITIVE) ON-STATE CURRENT



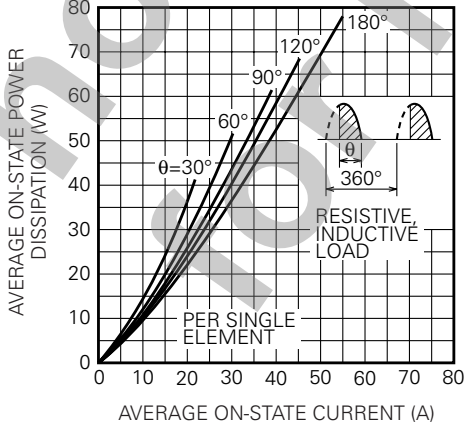
GATE CHARACTERISTICS



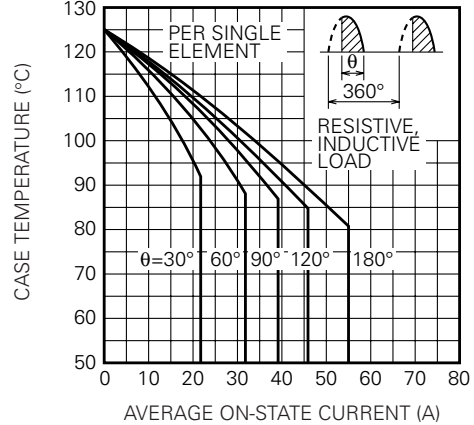
MAXIMUM TRANSIENT THERMAL IMPEDANCE (JUNCTION TO CASE)



MAXIMUM AVERAGE ON-STATE POWER DISSIPATION (SINGLE PHASE HALF WAVE)

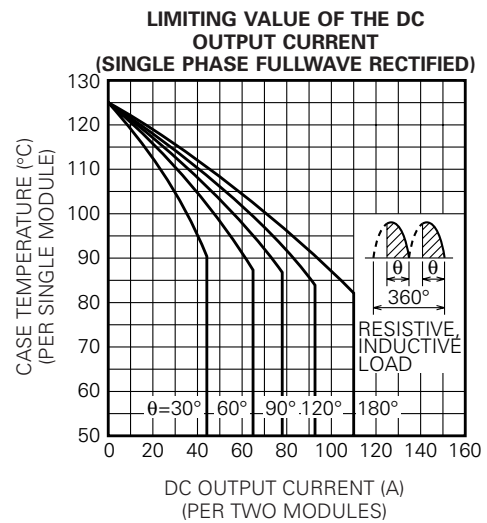
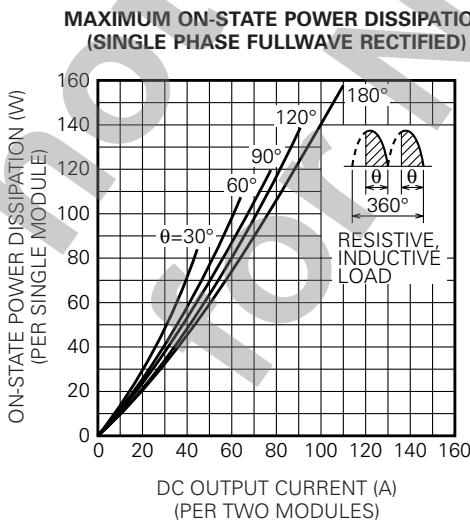
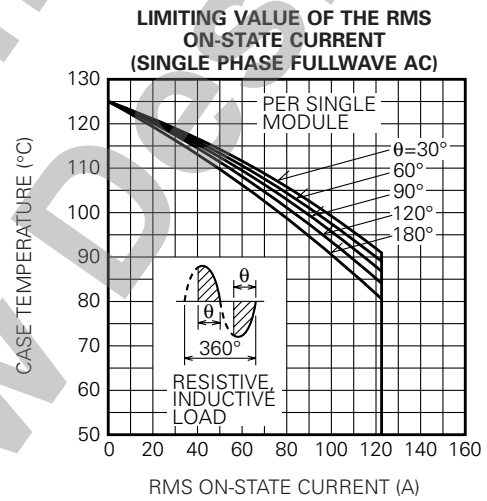
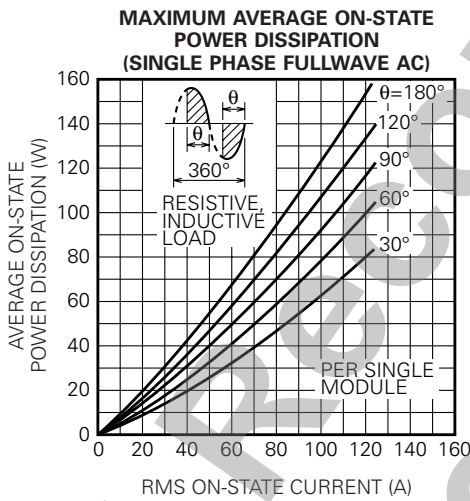
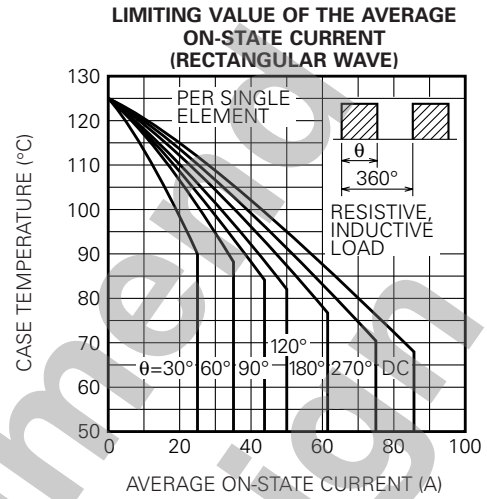
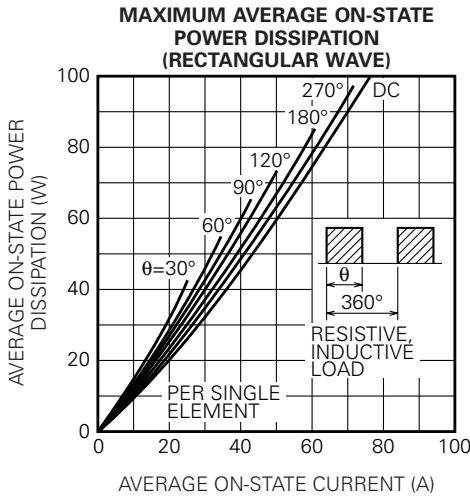


LIMITING VALUE OF THE AVERAGE ON-STATE CURRENT (SINGLE PHASE HALF WAVE)



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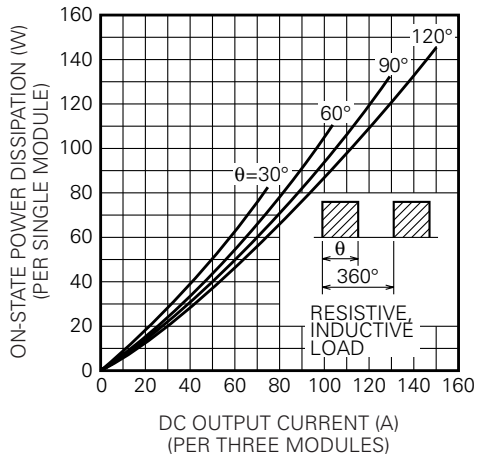
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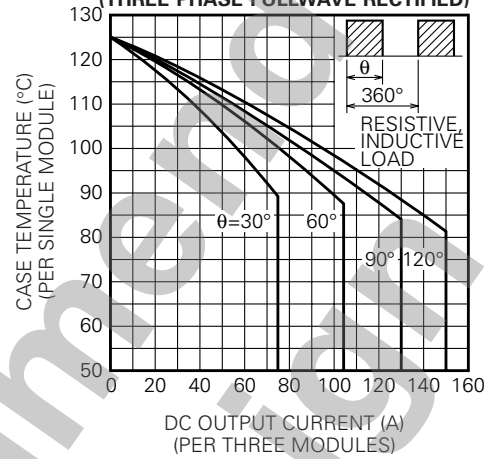
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**MAXIMUM ON-STATE POWER DISSIPATION
(THREE PHASE FULLWAVE RECTIFIED)**



**LIMITING VALUE OF THE DC
OUTPUT CURRENT
(THREE PHASE FULLWAVE RECTIFIED)**



not Recommended for New Design